

DERWENT-ACC-NO: 2000-003894

DERWENT-WEEK: 200343

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TITLE: Scanning circuit in liquid crystal display device -
generates dummy scan period during occurrence of
lacuna,
in retrace line interval

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PRIORITY-DATA: 1998JP-0086541 (March 31, 1998)

PATENT-FAMILY:

PUB-NO MAIN-IPC	PUB-DATE	LANGUAGE	PAGES
JP 11282433 A G09G 003/36	October 15, 1999	N/A	005
JP 3378796 B2 G09G 003/36	February 17, 2003	N/A	005

APPLICATION-DATA:

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JP 11282433A 1998	N/A	1998JP-0086541	March 31,
JP 3378796B2 1998	N/A	1998JP-0086541	March 31,
JP 3378796B2	Previous Publ.	JP 11282433	N/A

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ABSTRACTED-PUB-NO: JP 11282433A

BASIC-ABSTRACT:

NOVELTY - Scan signal monitor (2) measures signal duration during scan period.

During the retrace line interval, a lacuna is generated. A dummy scan signal

generator (5) generates signal to provide dummy scan period during lacuna

period, based on which controller outputs signal to liquid crystal cell driver.

USE - In liquid crystal display (LCD) device.

ADVANTAGE - As vertical retrace line interval is taken into consideration,

quality of display is maintained. DESCRIPTION OF DRAWING(S) - The figure shows

the block diagram of the LCD device. (2) Scan signal monitor; (5) Dummy scan

signal generator.

CHOSEN-DRAWING: Dwg.1/3

TITLE-TERMS: SCAN CIRCUIT LIQUID CRYSTAL DISPLAY DEVICE GENERATE DUMMY SCAN

PERIOD OCCUR RETRACE LINE INTERVAL

DERWENT-CLASS: P81 P85 T04 U14

EPI-CODES: T04-H03B; T04-H03C2A; U14-K01A3;

SECONDARY-ACC-NO:

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